

Electronic Acknowledgement Receipt

EFS ID:	1773823
Application Number:	10770711
International Application Number:	
Confirmation Number:	2278
Title of Invention:	PREDICTING DEFECT FUTURE EFFECTS IN INTEGRATED CIRCUIT TECHNOLOGY DEVELOPMENT TO FACILITATE SEMICONDUCTOR WAFER LOT DISPOSITION
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Filer Authorized By:	Mikio Ishimaru
Attorney Docket Number:	H0897
Receipt Date:	14-MAY-2007
Filing Date:	02-FEB-2004
Time Stamp:	17:51:29
Application Type:	Utility

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	1000-235_ISSUE_Fee.pdf	117642	no	1

Warnings:

Information:					
2	Change of Address	1000-235_Fee_Address.pdf	66110	no	1
Warnings:					
Information:					
Total Files Size (in bytes):			183752		
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